

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Takao NISHIMURA, et al.

Serial Number: Not Yet Assigned

Filed: January 26, 2004

Customer No.: 38834

For: A STACKED SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

January 26, 2004

Sir:

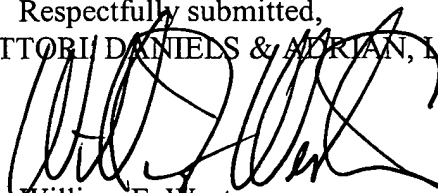
In compliance with 37 CFR 1.56, Applicant(s) call(s) to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

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Respectfully submitted,
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Enclosures: PTO-1449; References (2)

INFORMATION DISCLOSURE STATEMENT PTO-1449	Attv. Docket No. 042057	Serial No. New Appln.
	Applicant(s): Takao NISHIMURA, et al.	
	Filing Date: January 26, 2004	Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
	AA					
	AB					
	AC					
	AD					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
AE 6-252334	09/09/94	Japan	Abstract & Discussed in the Spec.
AF 2002-158312	05/31/02	Japan	Abstract & Discussed in the Spec.
AG			
AH			
AI			

OTHER DOCUMENTS

	AJ	
	AK	
Examiner	Date Considered	